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A New Feed-Forward Control for Dynamics Improvement in a Dual-Input DC–DC Converter for Hybrid Vehicle Applications

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Abstract: In this study, a double-input single-output bidirectional DC–DC converter is considered. This particular architecture allows less switches to be used than a conventional solution. A new feed-forward current control for this DC–DC converter with three switches is presented in this paper. The modulation technique proposed in the literature for the aforementioned converter leads to a consistent loss reduction at low load, exploiting the DCM. As a drawback, when using this control strategy, the dynamic response worsens significantly. To speed up the control, a feed-forward approach is designed and implemented using a simplified converter electrical model. The proposed strategy is compared with the conventional PI controller, and it is validated and verified through simulation results in the MATLAB/Simulink/PLECS environment and through experimental tests using a converter prototype.

Keywords: DC–DC converter; feed forward; multi-input converter; DCM; control strategy



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1. Introduction

DC–DC converters play a key role in environmental sustainability [1]. Renewable energies and storage are often connected to a common DC-bus system with a DC–DC converter [2,3]. In particular, DC–DC converters are employed in photovoltaic systems in [4–7]. Moreover, DC–DC converters are also used in electric mobility to connect storage systems to a vehicle's DC link [8–11]. The integration of a fuel cell system in a hybrid vehicle is explored in [12], while the combined use of batteries and supercapacitors (SCs) has been investigated in [13]. As a matter of fact, several studies can be found in the technical literature on the development of an optimal design and control of DC–DC converters, in particular when storage sources, such as batteries and supercapacitors are employed, where SCs typically handle the demanded power spikes, whereas the average demanded power is provided by the battery [14]. Indeed, one of the major challenges in a battery/SC hybrid energy storage systems is to design optimal control, which can provide good power split performance [15,16]. When different DC sources are connected to the same DC bus, it could be convenient from the point of view of energy efficiency to use multi-input and/or multi-output converters [17]. Various configurations are proposed in the technical literature, both with galvanic insulation [18–21], such as the one presented in [22] exploiting zero-voltage switching (ZVS), and without galvanic insulation. In particular, the isolated solutions introduced in [23,24] are single-input dual-output topologies, whereas a multi-input architecture is explored in [25–27]. The double-input solution proposed in [28] allows an efficiency higher than 95% to be achieved, again exploiting ZVS.

Recently, a new dual-input bidirectional DC–DC converter was proposed in [29]. This innovative converter structure does the same job as two half-bridge converters with a parallel connection; however, it only uses three switches (four switches are needed

when using two half-bridge converters). In addition to that, the three-switch converter proposed for the first time in [29] ensures higher efficiency in comparison to two half-bridges converters, as explained in [29], and analyzed in detail in [30]. Nevertheless, with the modulation strategy proposed in [29], the converter works in continuous conduction mode (CCM) even at low load (i.e., it allows current inversion during the switching period); as a consequence, the efficiency drops significantly in this working region.

The converter is then reintroduced in [31], while, in order to improve the efficiency in the low-load region, a new modulation strategy is proposed in [32], exploiting the discontinuous conduction mode (DCM) at low load. Moreover, the control strategy proposed in [32] optimizes the current paths even in the high-load region. Therefore, the efficiency is increased in regard to several operating points. Loss reduction is higher than 6% in a very wide range of the working area and is higher than 20% in almost all the DCM region.

However, with the modulation strategy proposed in [32], the system becomes strongly non-linear. Indeed, as it is shown furtherly and more in detail in this paper, a small switch duty cycle variation in the CCM region causes a high current variation, whereas, on the contrary, a high duty cycle variation causes a small current variation in the DCM region. It follows that the proportional–integral (PI) controller gains that assure stability in the CCM region make the converter response in the DCM significantly slow. The current response speed is particularly critical when voltage control is implemented. As a matter of fact, if the current loop is slow, it results in a significant voltage variation during transients, or in voltage loop instability. Since the boundary between the CCM and DCM depends on the working conditions and several variables, trying to use a PI controller with variable gains is a hard task and generally leads to converter instability.

In this paper, this particular three-switch converter structure is considered, and a simplified model is proposed to overcome the complexity of the system, especially in the DCM. The model is used to implement a feed-forward approach in regard to the modulation proposed in [32].

Please note that for this particular converter topology only the classic PI controller has been presented in the technical literature, therefore this feed-forward methodology represents the first control strategy proposed to improve the performance in the DCM region, and it cannot be compared with other techniques. The effectiveness of the feed-forward approach has been validated with simulations and experimental results, using a converter prototype. The experimental tests demonstrated an improvement in terms of the current loop bandwidth and, consequently, in terms of the DC-link voltage loop stability.

The paper is structured as follows. A brief description of the converter and of the control proposed in [32] is given in Section 2. The converter DCM model is discussed in Section 3, together with a description of the feed-forward approach. The simulation results are contained in Section 4, while the experimental setup and the results are presented in Section 5. Finally, the conclusions are set out in Section 6.

2. Converter Structure and Control

The converter architecture is shown in Figure 1. The converter is bidirectional and the sum of the dual-input voltage and V_2 should be lower than the DC-link voltage V_{out} .

The control proposed in [32] is shown in Figure 2. It allows the converter to work in the DCM at low load (i.e., it prevents current inversion during the switching period), and it optimizes the current paths for loss reduction even in the CCM. The gains K_1 and K_2 , and the connection between the outputs and the switch gate pulse vary, according to the working condition. Five working conditions are considered in the control described in Figure 2, as follows:

1. $I_1 > 0, I_2 > 0$
2. $I_1 > 0, I_2 < 0$
3. $I_1 < 0, I_2 > 0$
4. $I_1 < 0, I_2 < 0, I_1 > I_2$
5. $I_1 < 0, I_2 < 0, I_1 < I_2$

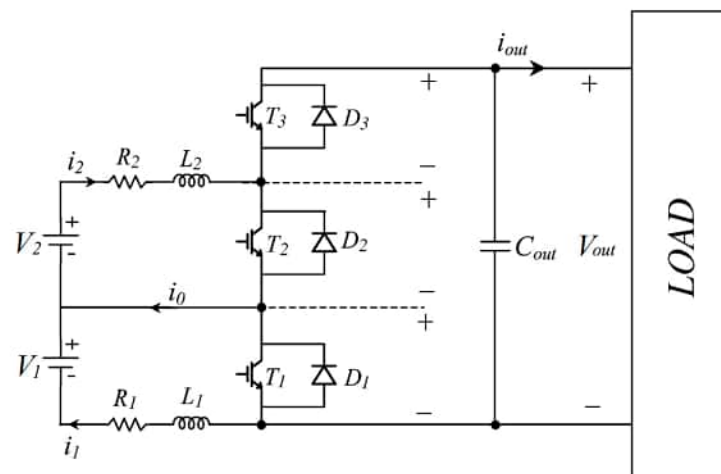


Figure 1. Three-switch dual-input bidirectional DC-DC converter.

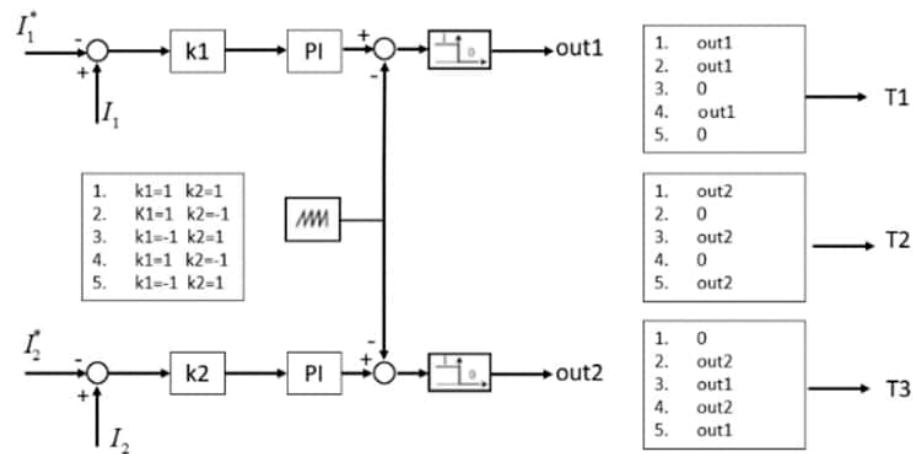


Figure 2. The control scheme for the three-switch converter.

Please note that i_1 and i_2 are the current instantaneous values, whereas I_1 and I_2 are the current average values during the duty cycle.

If both currents are positive, the instantaneous values are always different (i.e., current path D, shown in Figure 3, does not occur), and both currents work in the CCM. I transfer function between I_1 and the duty cycle (m_1) of switch T_1 is given by (1):

$$\frac{I_1}{m_1} = \frac{V_{out}}{(R_1 + sL_1)(sT_p + 1)} \quad (1)$$

where V_{out} is the output voltage, L_1 and R_1 are the inductance and resistance of the inductor in the series in regard to the storage, as shown in Figure 1, whereas T_p is the converter delay, which can be considered 1.5 times the switching period T_s . Please note that, as it is commonly set in the control of converters, this value of $1.5 \cdot T_s$ is the average between $1 \cdot T_s$ (the waiting time until the reference value of PWM changes) and $2 \cdot T_s$ (the time before the reference value changes plus an entire PWM period). An analogous equation correlates I_2 with m_2 . Considering (1) that one can tune the PI controller gains using, for example, the phase margin criterion. However, with this set of gains, the current loop becomes significantly slow in the DCM (i.e., in the low-load region). Indeed, with the typical parameters of a converter, from (1) it follows that a small duty cycle variation leads to a significant current variation. On the contrary, in the DCM zone, as it is shown in Section 3, a significant duty cycle variation leads to a small current variation.

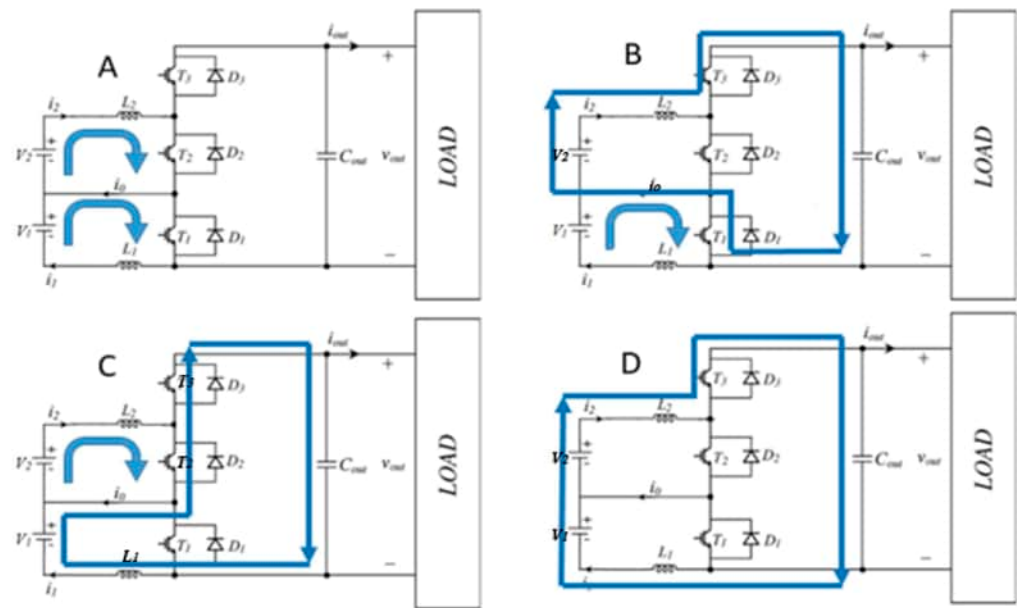


Figure 3. Current paths in condition 1 (I_1 and $I_2 > 0$). (A) Path A; (B) Path B; (C) Path C; (D) Path D.

This study proposes a feed-forward technique to increase the converter dynamics at low load.

3. Feed-Forward Approach Derivation

When both I_1 and I_2 are positive (condition 1), one has four different current paths, which are reported in Figure 3. In Table 1, expressions describing the different current slopes, which are the current derivatives during the switching period, and the IGBT configurations for each path, are shown.

Table 1. Current slopes and IGBT configuration in condition 1.

Path	IGBT Config.	Current Slope I_1	Current Slope I_2
A	T_1 on T_2 on T_3 off	$\frac{V_1}{L_1}$	$\frac{V_2}{L_2}$
B	T_1 on (or T_1 off and $i_1 < i_2$) T_2 off T_3 off	$\frac{V_1}{L_1}$	$\frac{V_{out}-V_2}{L_2}$
C	T_1 off T_2 on (or T_2 off and $i_2 < i_1$) T_3 off	$\frac{V_{out}-V_1}{L_1}$	$\frac{V_2}{L_2}$
D	T_1 off T_2 off T_3 off	$\frac{V_{out}-V_1-V_2}{L_1+L_2}$	$\frac{V_{out}-V_1-V_2}{L_1+L_2}$

As mentioned in Sections 1 and 2, with the modulation strategy proposed in [32] and the converter working in the DCM, the dynamic response worsens significantly. In order to increase the dynamic behavior of the converter, it is therefore necessary to apply an opportune feed-forward technique. In this way, the feed-forward inputs guarantee a fast response, with a small error, which is then annulled by the PI controller, with a time constant that can be relatively high.

While working in the DCM, the relationship between the gate pulse duty cycles (GPDs) and the current references vary according to the current signs, the current values, depending on which current has a higher value. Not only does the relationship between

the GPDs and current references vary according to the working condition, but, moreover, in regard to some working points (e.g., when current path D occurs), the analytic relationship becomes very complex and cannot be implemented in the converter control.

For these reasons, a simplified approach is carried out in this study, to find simplified relations between GPDs and current references.

3.1. Methodology

Figures 4 and 5 show the instantaneous current waveforms, with i_1 (red) and i_2 (blue) positive and negative, respectively. One can note the current overlap in both cases. During the overlap (current path D in Figure 3), the current slopes become $\frac{V_{out}-V_1-V_2}{L_1+L_2}$, instead of $\frac{V_{out}-V_1}{L_1}$ and $\frac{V_{out}-V_2}{L_2}$. Please note that current overlap (path D) occurs, while currents have the same instantaneous value and they are both decreasing, whereas, while both currents increase, the slopes are $\frac{V_1}{L_1}$ and $\frac{V_2}{L_2}$, respectively (current path A).

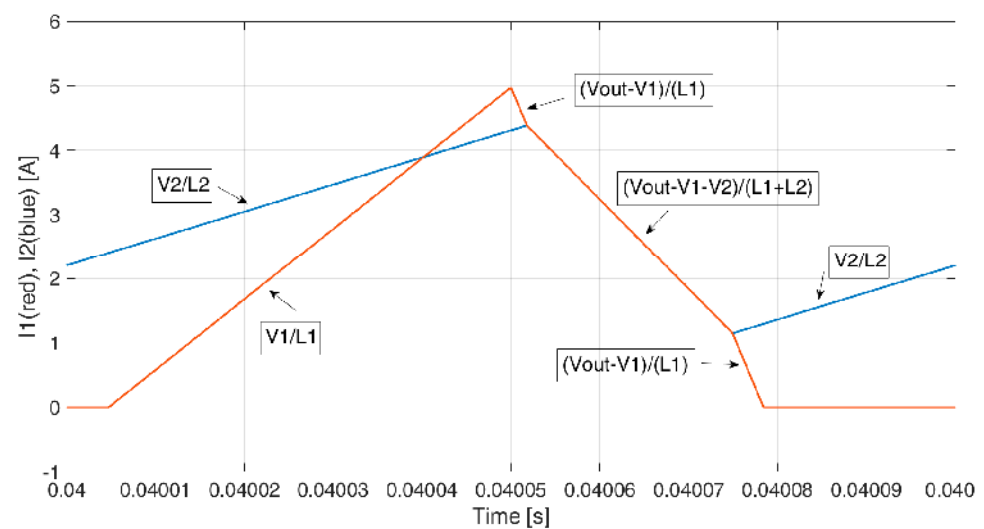


Figure 4. The i_1 (red) and i_2 (blue) waveforms when $I_1 > 0$ and $I_2 > 0$ (condition 1) in condition 1 (I_1 and $I_2 > 0$).

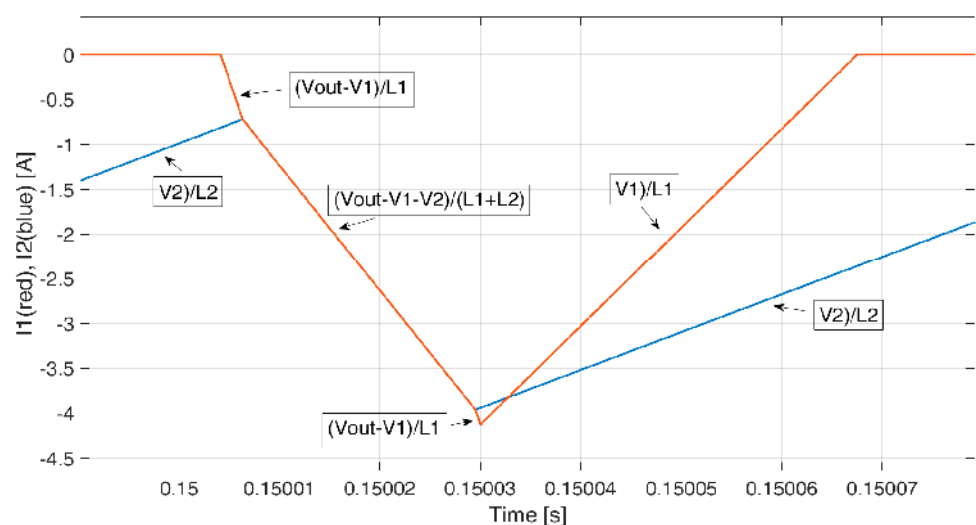


Figure 5. The i_1 (red) and i_2 (blue) waveforms when $I_1 < 0$ and $I_2 < 0$ (condition 4).

From the current waveforms shown above, one can note that the analytic relations between the GPDs and current reference become very complex in these cases, and they

depend on many variables. For this reason, a simplified approach is carried out in this paper, exploiting a simplified converter model.

The goal of the feed-forward method is to calculate the correct values for the three GPDs in order to satisfy the current references (i.e., to achieve certain average values for the two currents). The analytic equations are evaluated when there is no overlap (NOV) and when $i_1 = i_2$, i.e., when there is a total overlap (TOV). These two cases are identified as the two limit conditions. On such a basis, the equations obtained in these two cases are then interpolated to obtain the feed-forward values in the conditions with a partial overlap. Hence, a parameter K measuring the overlap ratio is defined.

The simplified converter model is obtained by evaluating six different conditions, as follows:

- (1) $I_1 < I_2, I_1 > 0, I_2 > 0$ (NOV and TOV)
- (2) $I_1 < I_2, I_1 < 0, I_2 > \hat{I}_2$ (NOV)
- (3) $I_1 < I_2, I_1 < 0, I_2 < \hat{I}_2$ (NOV and TOV)
- (4) $I_1 > I_2, I_1 > 0, I_2 > 0$ (NOV and TOV)
- (5) $I_1 > I_2, I_1 > \hat{I}_1, I_2 > 0$ (NOV)
- (6) $I_1 > I_2, I_1 < \hat{I}_1, I_2 < 0$ (NOV and TOV)

where $\hat{I}_1$ and $\hat{I}_2$ will be defined in the following Section and they are always positive quantities. For this reason, in the second and the fifth conditions, the currents have opposite signs and, therefore, an overlap is not possible. In this paper, only the first case was described in detail, contained in Appendix A, but the same procedure was used in this study to find the feed-forward outputs for the other five conditions.

3.2. Interpolation Process

The interpolation process is used to find the feed-forward output, while there is a partial overlap. The current ripples, when both currents are in the CCM and do not overlap, are defined in Equations (2) and (3). If the currents are in the DCM and there is no overlap, it is easy to verify that the ripple is given by (4) and (5). Therefore, a parameter K , which measures the overlap ratio, is defined in (6). Since in (A5), (A12), (A30), and (A38), the current is under the square root, the duty cycles can be calculated as in (7).

$$di_1 = \frac{V_1(V_{out} - V_1)T_s}{L_1 V_{out}} \quad (2)$$

$$di_2 = \frac{V_2(V_{out} - V_2)T_s}{L_2 V_{out}} \quad (3)$$

$$di_{1real} = di_1^{(sign)} \sqrt{\frac{I_1}{di_1/2}} \quad di_{1real} \in [-di_1; di_1] \quad (4)$$

$$di_{2real} = di_2^{(sign)} \sqrt{\frac{I_2}{di_2/2}} \quad di_{2real} \in [-di_2; di_2] \quad (5)$$

$$K = \left| \frac{2(I_2 - I_1)}{|di_{1real} + di_{2real}|} \right| \in [0; 1] \quad (6)$$

$$m_x = \sqrt{K}m_{x(NOV)} + (1 - \sqrt{K})m_{x(TOV)} \quad (7)$$

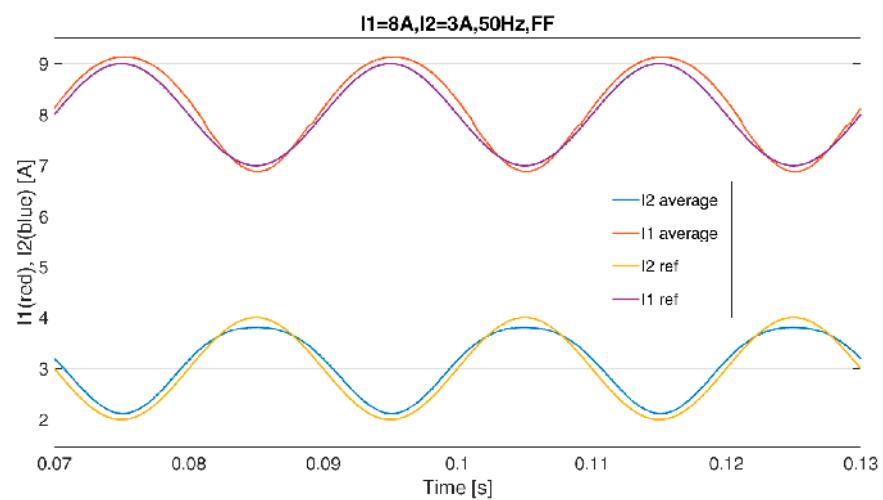
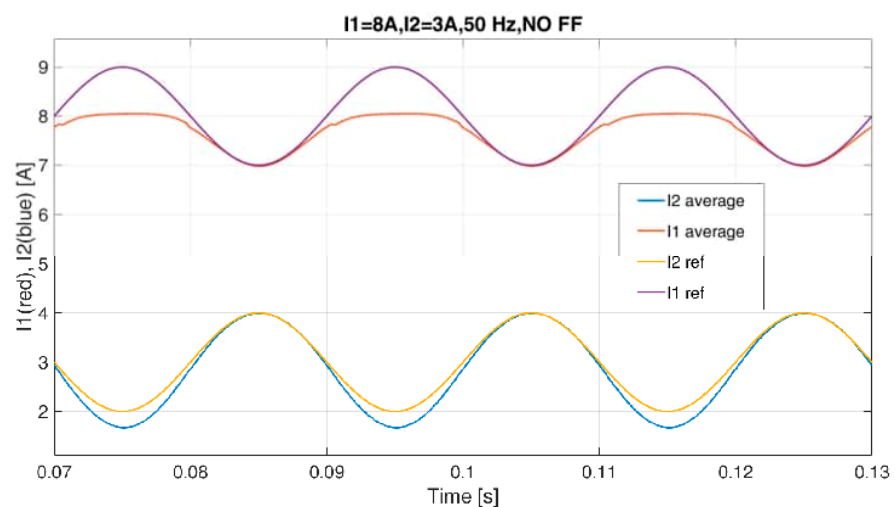
4. Simulation Results

In this Section, some simulation results have been reported to compare the feed-forward (FF) method presented in this paper with the previous control in [30,32]. The system has been implemented in the MATLAB/Simulink/PLECS environment, and the converter parameters are reported in Table 2.

Table 2. Converter parameters.

Parameter	Value	Parameter	Value
IGBT type	SKM400GA124D	C	4.7 mF
V_1	12 V	V_{out}	150 V
L_1	950 μ H	f_{sw}	10 kHz
L_2	910 μ H	I_1 limit	0.55 A
V_2	22 V	I_2 limit	1.03 A
R1	0.01 Ω	R2	0.01 Ω

Two sinusoidal references have been imposed for I_1 and I_2 , 8 A and 3 A, respectively (please note that 8 and 3 are the average values of the signal, the sine amplitude is 1), at the frequencies of 50 Hz and 250 Hz. In Figures 6 and 7, the two average currents are shown together with their reference value, in the case of FF and NO FF, respectively, for the test at 50 Hz. Figures 8 and 9 report the results for the test at 250 Hz, while in Figure 10 the two instantaneous values are reported in the case of FF at 250 Hz, where it can be seen that the two currents overlap and I_2 is in the DCM.

**Figure 6.** Simulated average currents I_1 and I_2 with their reference values at 50 Hz, FF.**Figure 7.** Simulated average currents I_1 and I_2 with their reference values at 50 Hz, NO FF.

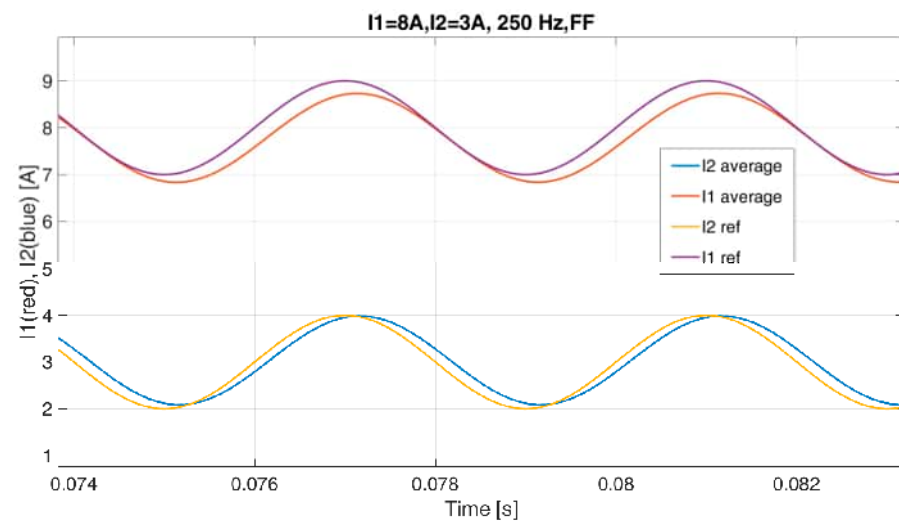


Figure 8. Simulated average currents I_1 and I_2 with their reference values at 250 Hz, FF.

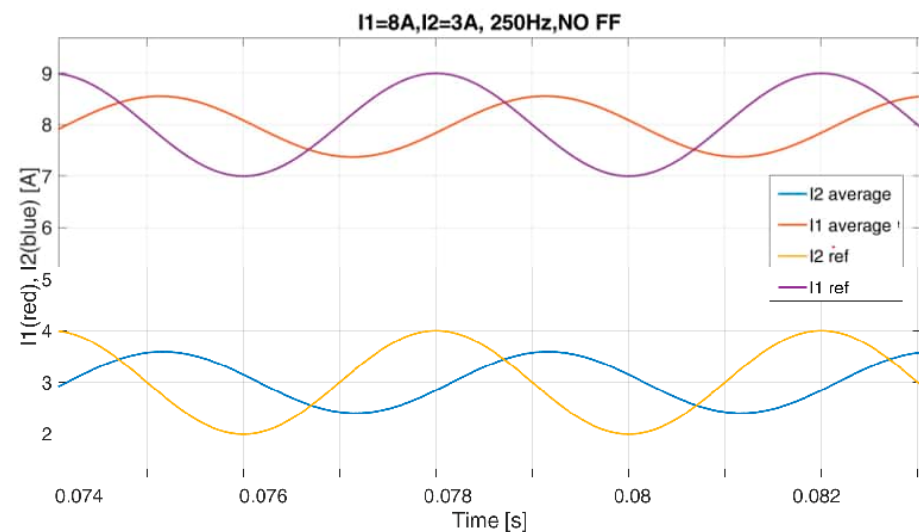


Figure 9. Simulated average currents I_1 and I_2 with their reference values at 250 Hz, NO FF.

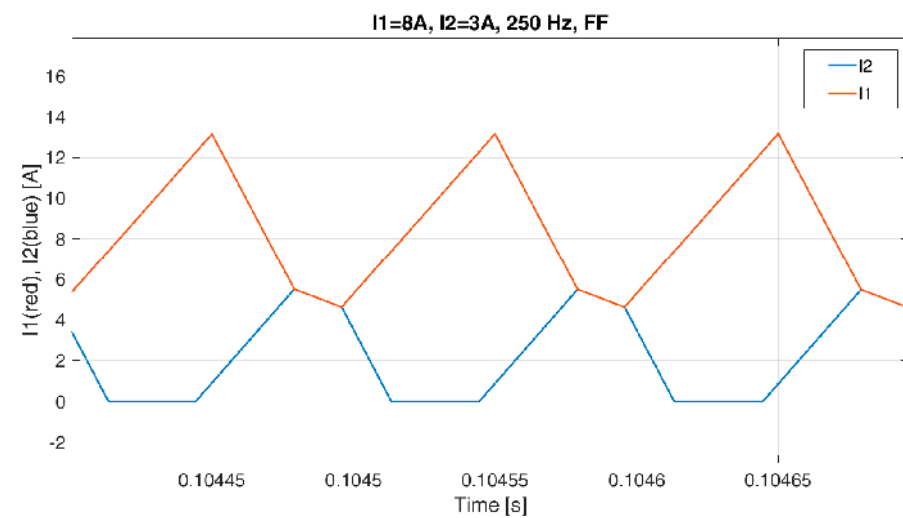


Figure 10. Simulated instantaneous currents I_1 and I_2 , with their reference values at 250 Hz, FF.

From these simulation results it is possible to notice a better performance in the case of the proposed feed-forward method. As a matter of fact, in the 50 Hz test, the reference signal for I_1 is not accurately followed in the case of NO FF in Figure 7, while a delay and an attenuation can be highlighted in the average currents in the test at 250 Hz, as reported in Figure 9.

In the following simulation tests a more critical working condition has been chosen, compared to the previous results. As a matter of fact, having both the two currents, the same small amplitude sinusoidal input as a reference leads the control to work close to the total overlap (TOV) condition in the DCM. In addition, every time the reference current crosses zero a discontinuity in the control is encountered.

The I_2 waveform is plotted together with its reference both for the FF and NO FF control for a sinusoidal reference signal at 5 A (null average value in this case), and a 3 Hz frequency, as shown in Figure 11a,b. From the results it can be noticed that the reference is better in the case of the FF.

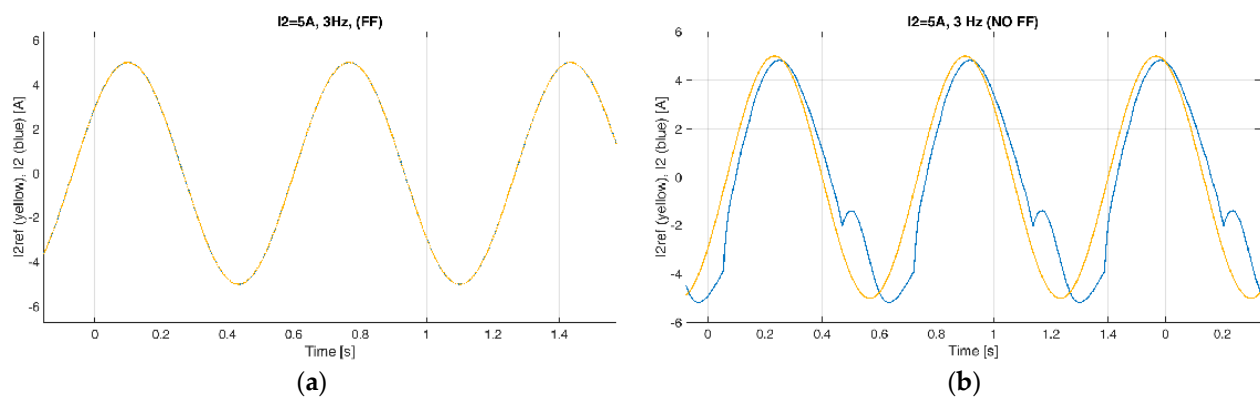


Figure 11. Simulated current I_2 and the reference, 5 A, 3 Hz: (a) with FF, (b) without FF.

For the last simulation test, an outer voltage loop control has been implemented in order to keep the V_{out} at a constant value of 150 V, while the current reference I_2 is kept at 5 A. Please note that the I_1 reference is provided, instead, by the voltage control loop. The test involves a load step variation with a 100 Ω resistor connected to the DC link. The DC-link voltage is shown in Figure 12 both for FF and NO FF, where it is evident that the DC-link voltage drop is significantly reduced with the use of the proposed FF.

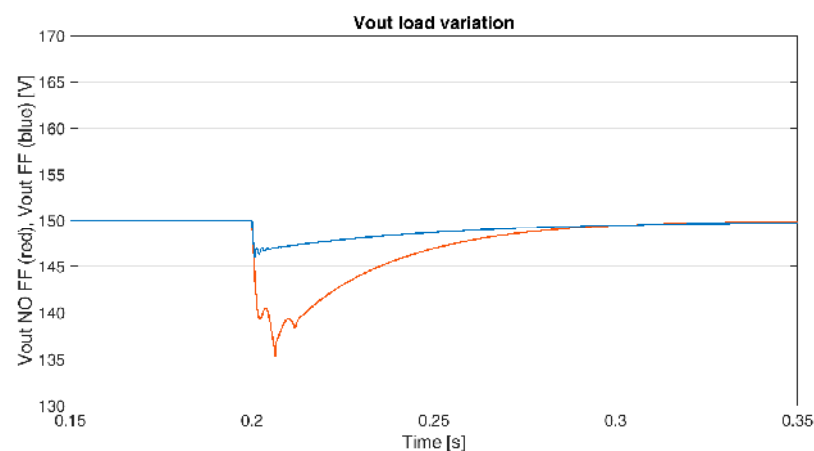


Figure 12. Simulated V_{out} FF (blue) and NO FF (red) during a load step variation.

Please note that these last two simulations have been replicated in the Section 5 shown in the following Section and, the results are reported in Figure 19a,b, and Figure 20, respectively.

5. Experimental Results

In order to validate the proposed feed-forward (FF) approach, experimental tests were carried out in different operating conditions with both FF and NO FF. The test bench is shown in Figure 13. The control is implemented in a DSpace MicroLabBox (Paderborn, Germany). The Dspace system outputs are sent to the converter prototype, as illustrated in Figure 13. A supercapacitor (charged at 22 V) and a 12 V battery are used as the converter input sources. Please note that these values do not represent a realistic automotive application, since this converter prototype is a reduced-scale one (the development of a new converter prototype with a power level compatible with a typical automotive application and higher DC source voltage levels has already been taken into account for further studies). A 100 Ω resistor is connected to the DC link through a switch for load variation tests.



Figure 13. Test bench.

Since for this particular converter topology it is possible to control two quantities among I_1 , I_2 , and V_{out} , concerning the existing two degrees of freedom, all the experimental tests were performed with a current reference for I_2 and a PI controller voltage loop to keep V_{out} at a constant value of 150 V.

The I_1 (magenta), I_2 (blue), and V_{out} (green) are plotted in Figure 14 (FF) and Figure 15 (NO FF), with a current reference variation from +5 A to −5 A for I_2 (blue line). Please note that the I_1 reference is given by the voltage loop and I_1 (magenta line) changes to keep a constant DC-link voltage.

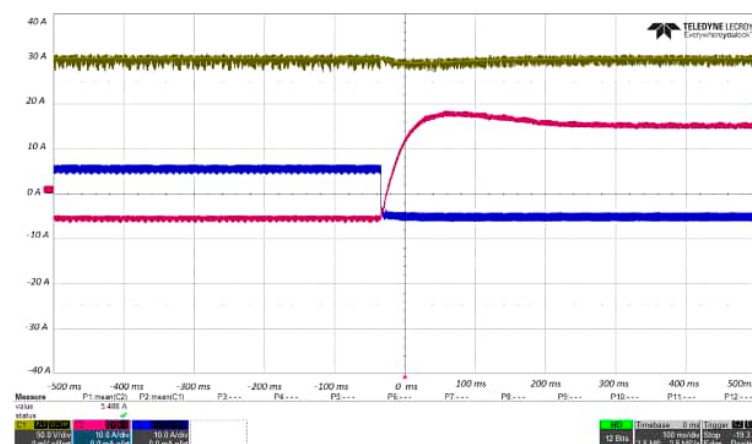


Figure 14. The V_{out} (green, C1, 50 V/div), i_1 (magenta, C2, 10 A/div), and i_2 (blue, C3, 10 A/div) during an I_2 variation from +5 A to −5 A (FF).

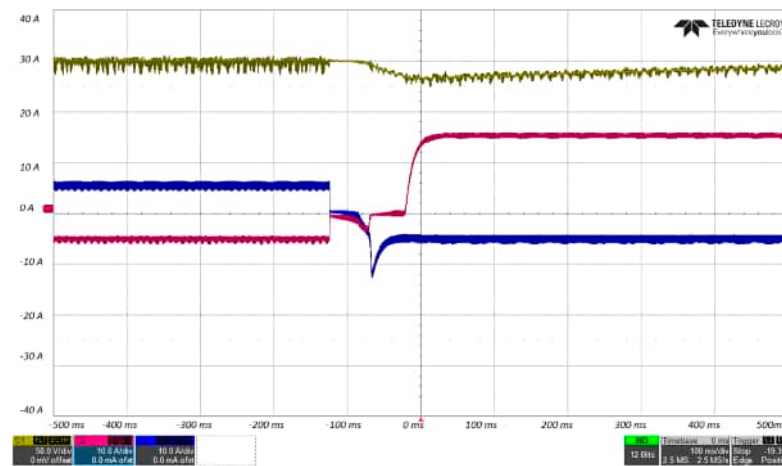


Figure 15. The V_{out} (green, C1, 50 V/div), i_1 (magenta, C2, 10 A/div), and i_2 (blue, C3, 10 A/div) during an I_2 variation from +5 A to −5 A (NO FF).

It is clear that the control with the FF approach is way faster and more stable than the NO FF control. In the following, the plots show the measures exported from the Dspace interface, instead of oscilloscope measures, as in Figures 14 and 15; this is done for two reasons, as follows:

1. A comparison between the I_2 input reference current and the actual current I_2 can be presented;
2. The Dspace interface computes the average value of a signal during a switching period, so a cleaner signal without ripples can be shown.

To determine the current control bandwidth in different working conditions, tests with a sinusoidal reference for I_2 were carried out. Please note that for this experimental test a much more critical working condition has been chosen, compared to the simulation results. As a matter of fact, having a small amplitude sinusoidal input as a reference for the two currents leads the control to work close to the total overlap (TOV) condition in the DCM. In addition, every time the reference current crosses zero, a discontinuity in the control is encountered.

The I_2 waveform is plotted, together with its reference both for the FF and NO FF control for a sinusoidal reference signal at 1 A, 1 Hz frequency, as shown in Figure 16a,b, and for a sinusoidal reference signal at 1 A, 3 Hz frequency, as shown in Figure 17a,b. Please note that the average I_2 current limit for the DCM is given by (41) and it is 1.03 A, as reported in Table 2. Hence, the converter is fully working in the DCM for I_2 .

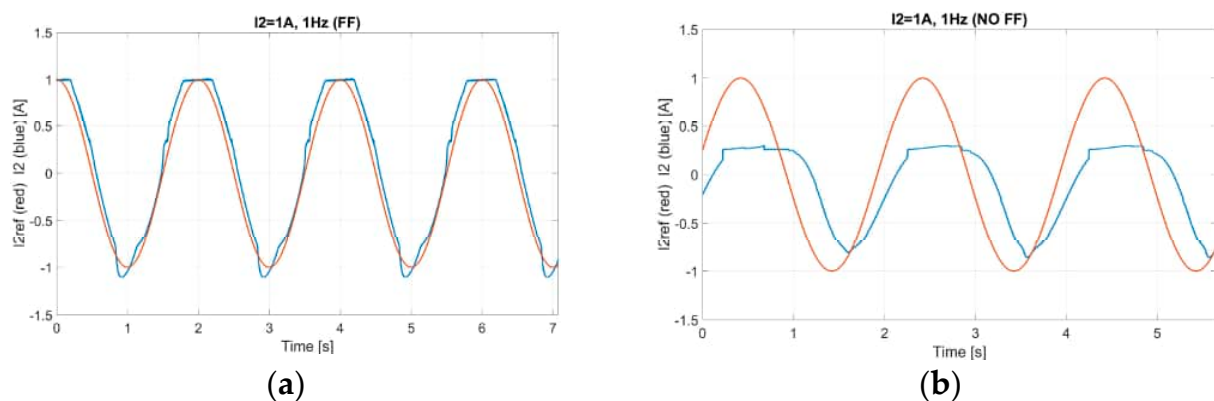


Figure 16. Experimental current I_2 and the reference, 1 A, 1 Hz: (a) with FF, (b) without FF.

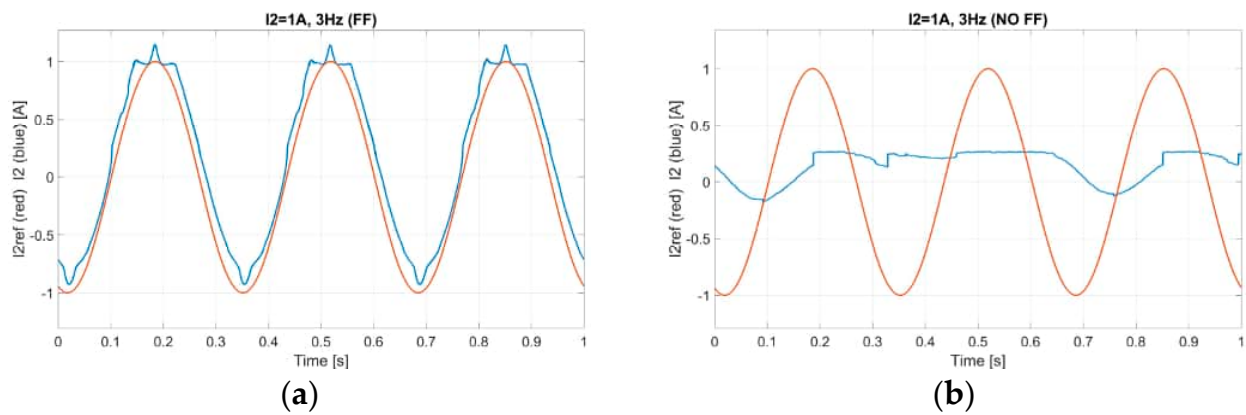


Figure 17. Experimental current I_2 and the reference, 1 A, 3 Hz: (a) with FF, (b) without FF.

The same experiment has been carried out with a reference current amplitude of 5 A to test the converter working both in the CCM and in the DCM. The I_2 waveform is plotted in Figure 18a,b for the test with 1 Hz frequency and in Figure 19a,b for the test at 3 Hz frequency. From the results shown in Figures 16–19, it is easy to appreciate the benefits of the proposed FF approach compared to the previous control. As a matter of fact, the NO FF control is unstable with the reference signal at 3 Hz, while the FF control guarantees a good level of tracking of the reference signal.

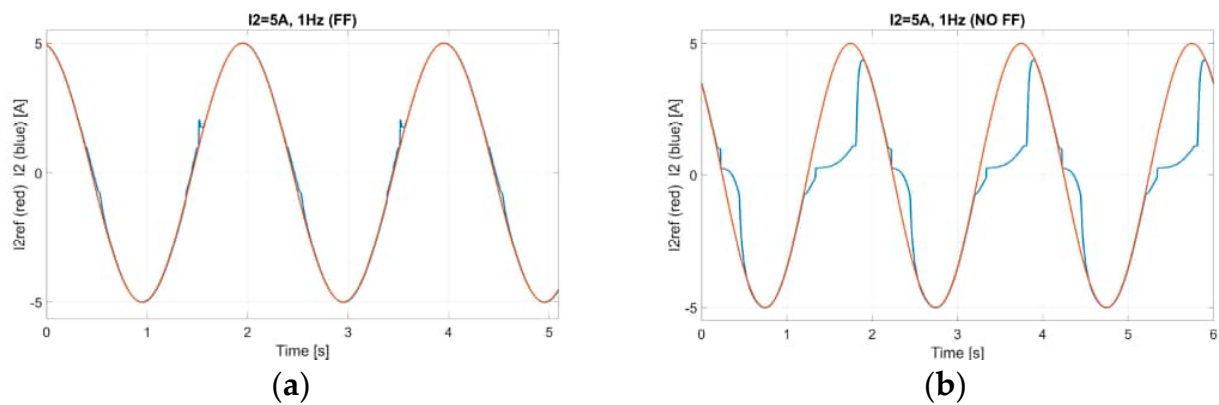


Figure 18. Experimental current I_2 and the reference, 5 A, 1 Hz: (a) with FF, (b) without FF.

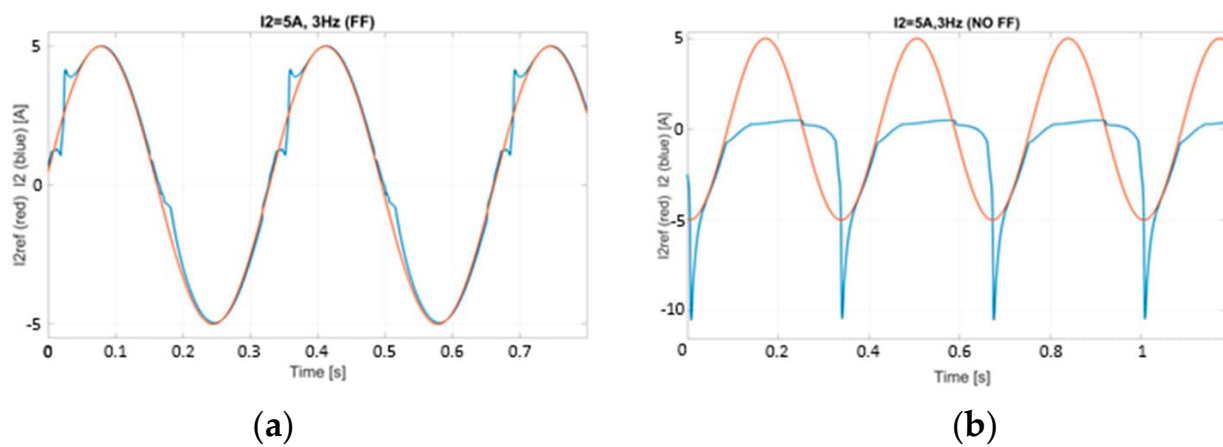


Figure 19. Experimental current I_2 and the reference, 5 A, 3 Hz: (a) with FF, (b) without FF.

The last experimental test is a load step variation with a 100 Ω resistor connected to the DC link, while the current reference I_2 is kept at 5 A; the I_1 reference is provided,

instead, by the voltage control loop. The DC-link voltage is shown in Figure 20, both for FF and NO FF, whereas the current waveforms are shown in Figures 21 and 22. Please note that in Figures 20–22, the signals of the different test acquisitions are superposed during the measure postprocessing. From Figure 20, it is clear that the DC-link voltage drop is significantly reduced, since the I_1 current loop (Figure 21) is significantly faster with the FF control. Moreover, I_2 remains almost constant with the FF, but drops significantly with NO FF (Figure 22).

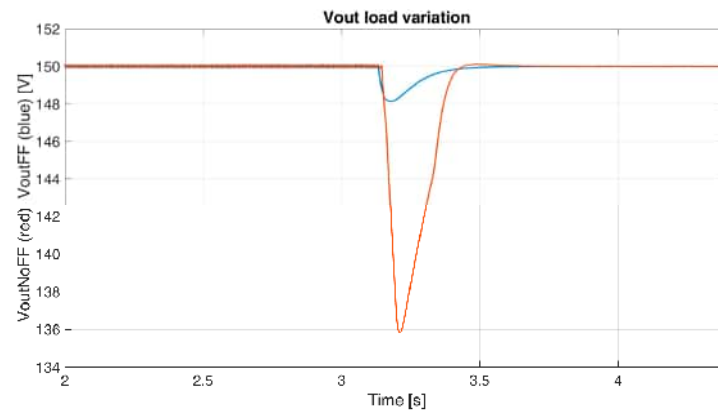


Figure 20. The V_{out} FF (blue) and NO FF (red) during a load step variation.

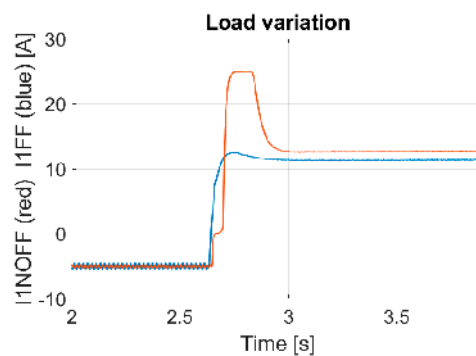


Figure 21. The I_1 with FF (blue) and NO FF (red) during a load step variation.

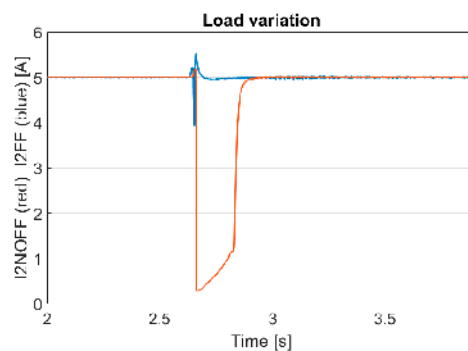


Figure 22. The I_2 with FF (blue) and NO FF (red) during a load step variation.

6. Conclusions

In this paper, a new feed-forward strategy for a dual-input innovative three-switch DC–DC converter is proposed. The modulation strategy proposed in the literature for this particular topology exploited the DCM, increasing the overall efficiency of the system [32].

Since, in the DCM, the converter becomes highly non-linear, a simplified converter model is proposed in this study and used to implement the feed-forward control in the

MATLAB/Simulink/PLECS environment, in order to perform simulation tests and provide a comparison with a previous control.

Afterwards, the proposed technique was implemented using a DSpace MicroLab-Box, and experimental tests on a laboratory prototype were carried out to compare the feed-forward control proposed in this paper with the previous control proposed in the technical literature. Both the simulation and the experimental results reported show that the proposed feed-forward strategy exhibits advantages in terms of stability and dynamics compared to the control without the feed-forward approach, together with a more robust response regarding the voltage drop during the load step variation. Ultimately, please note that the modulating technique described in [32] increases the converter's efficiency by allowing the exploitation of the DCM, but with undesirable dynamics; hence, the feed-forward approach presented in this paper is fundamental for the exploitation of the DCM control proposed in [32].

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Appendix A

Appendix A.1. Case 1: $I_1 < I_2$, $I_1 > 0$, $I_2 > 0$

Appendix A.1.1. NOV, Equation for m_2

Firstly, the relationship between m_2 and I_2 , while path D does not occur, will be evaluated. Please note that, in real cases, if $I_1 > 0$ and $I_2 > 0$, condition D always occurs if i_2 is in the DCM. The ideal i_2 waveform is shown in Figure 6, whereas the real i_2 waveform is shown in Figure A1. The equation obtained starting from the ideal waveform can be considered as the limit condition to be used in the interpolation process that is proposed below.

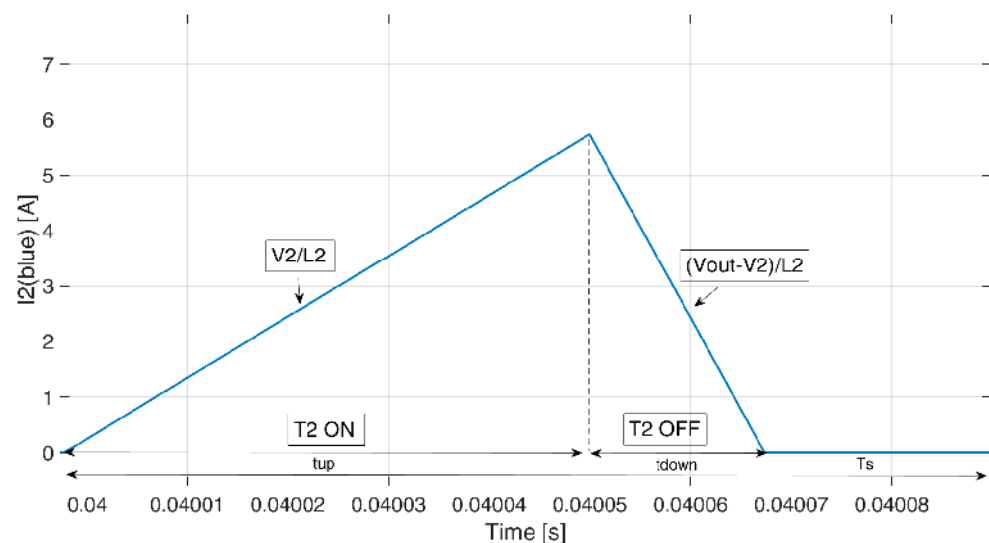


Figure A1. Ideal waveform of i_2 .

The i_2 real waveform is shown in Figure A2, together with the i_1 waveform. The rising time t_{up} is defined in (A1), the current peak value I_{peak} is defined in (A2), the falling time t_{down} in (A3), whereas the average current I_2 is evaluated in (A4). Combining (A1), (A2), (A3), and (A4) obtains (A5).

$$t_{up} = m_2 \times T_s \quad (A1)$$

$$I_{peak} = \frac{V_2}{L_2} \times t_{up} \quad (A2)$$

$$t_{down} = \frac{I_{peak}}{\frac{V_{out}-V_2}{L_2}} \quad (A3)$$

$$I_2 = \frac{I_{peak} \times (t_{up} + t_{down})}{2 \times T_s} \quad (A4)$$

$$m_2 = \sqrt{\frac{2L_2(V_{out} - V_2)}{V_2 T_s V_{out}}} \sqrt{I_2} \quad (A5)$$

The relationship in (A5) is valid until i_2 is in the DCM. Indeed, when the CCM occurs (i.e., when i_2 does not reach zero during the switching period), the condition in (A6) should be verified. Substituting (A5) in (A6), one obtains (A7). Therefore, m_2 is given by (A5), with the limitation that $m_2 \in \left[0; \frac{V_{out}-V_2}{V_{out}}\right]$.

$$\frac{V_2}{L_2} m_2 = \frac{V_{out} - V_2}{L_2} (1 - m_2) \quad (A6)$$

$$m_2 = \frac{V_{out} - V_2}{V_{out}} \quad (A7)$$

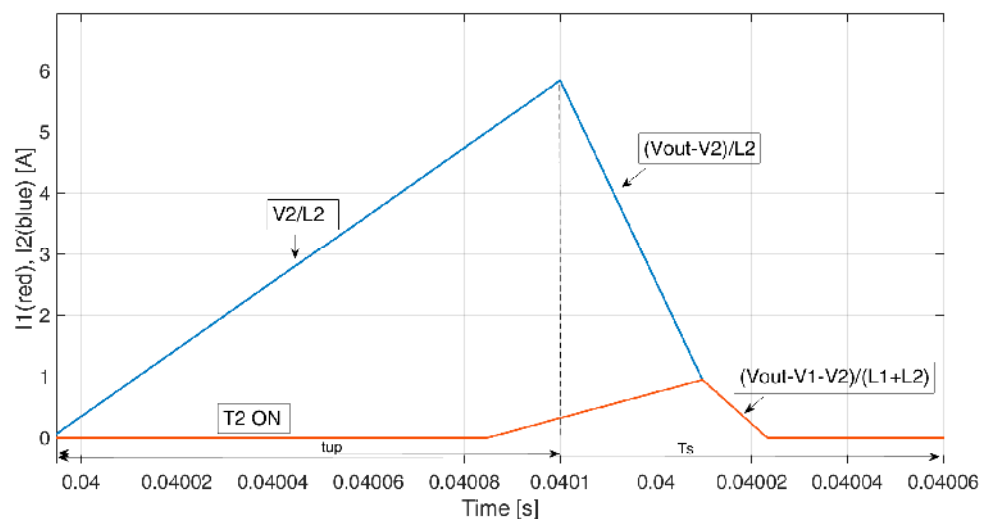


Figure A2. Real waveform of i_1 (red) and i_2 (blue).

Appendix A.1.2. NOV, $I_1 > \hat{I}_1$ Equation for m_1

The i_1 waveform is shown in Figure A3 (red), together with the i_2 waveform (blue). Please note that to prevent a heavy notation with long subscripts, the definitions of t_{up} , t_{down} and I_{peak} are valid from their definition until the next current waveform figure. Therefore, the same variable names are used at different times for different quantities. The t_{up} is now defined in (A8), I_{peak} is defined in (A9), t_{down} in (A10), and the average current I_2 in (A11). Substituting (A8)–(A10) in (A11) obtains (A12).

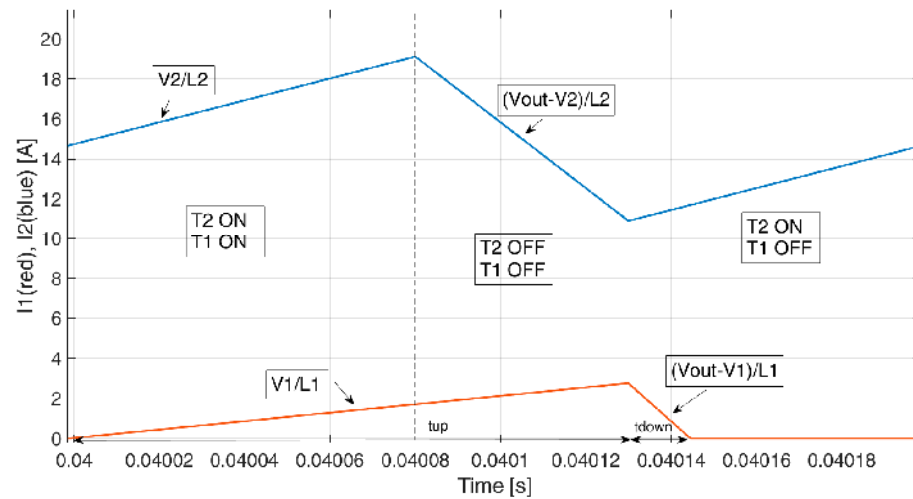


Figure A3. The i_1 (red) and i_2 (blue) waveforms when $I_1 < 0$ and $I_2 < 0$ (condition 4).

$$t_{up} = (m_1 + 1 - m_2)T_s \quad (A8)$$

$$I_{1peak} = \frac{V_1}{L_1} t_{up} \quad (A9)$$

$$t_{down} = \frac{I_{1peak}}{\frac{V_{out}-V_1}{L_1}} \quad (A10)$$

$$I_1 = \frac{I_{1peak}(t_{up} + t_{down})}{2T_s} \quad (A11)$$

$$m_1 = \sqrt{\frac{2L_1(V_{out}-V_1)}{T_s V_1 V_{out}}} \sqrt{I_1} + m_2 - 1 \quad (A12)$$

When the CCM occurs, the condition in (A13) should be verified. Substituting (A12) in (A13) obtains (A14).

Therefore, m_1 is given by (A12), with the limitation that $m_1 \in \left[0; \frac{V_{out}-V_1}{V_{out}}\right]$.

$$\frac{V_1}{L_1}(m_1 - m_2 + 1) = \frac{V_{out}-V_1}{L_1}(m_2 - m_1) \quad (A13)$$

$$m_1 = \frac{V_{out}-V_1}{V_{out}} \quad (A14)$$

It can be noted that when $m_1 = 0$, i_1 is not zero, since it increases while i_2 is decreasing, as shown in Figure A4. The I_1 value, while $m_1 = 0$, is defined as $\hat{I}_1$, and it is calculated according to the following information.

The I_1 , while i_2 is in the CCM and $m_1 = 0$, defined as $\hat{I}_{1NOV}$, can be evaluated substituting $m_1 = 0$ and $m_2 = \frac{V_{out}-V_2}{V_{out}}$ in (A12) obtaining (A15). While i_2 tends towards zero, I_1 also tends towards zero, as specified in (A16). The feed-forward value is given by the interpolation of $\hat{I}_{1NOV}$ and $\hat{I}_{1TOV}$. The interpolation process is described in Section 3.2.

$$\hat{I}_{1NOV} = \frac{T_s V_1 V_2^2}{2L_1 V_{out}(V_{out}-V_1)} \quad (A15)$$

$$\hat{I}_{1TOV} = 0 \quad (A16)$$

The same process can be carried out to define $\hat{I}_2$, $\hat{I}_{2TOV}$, and $\hat{I}_{2NOV}$.

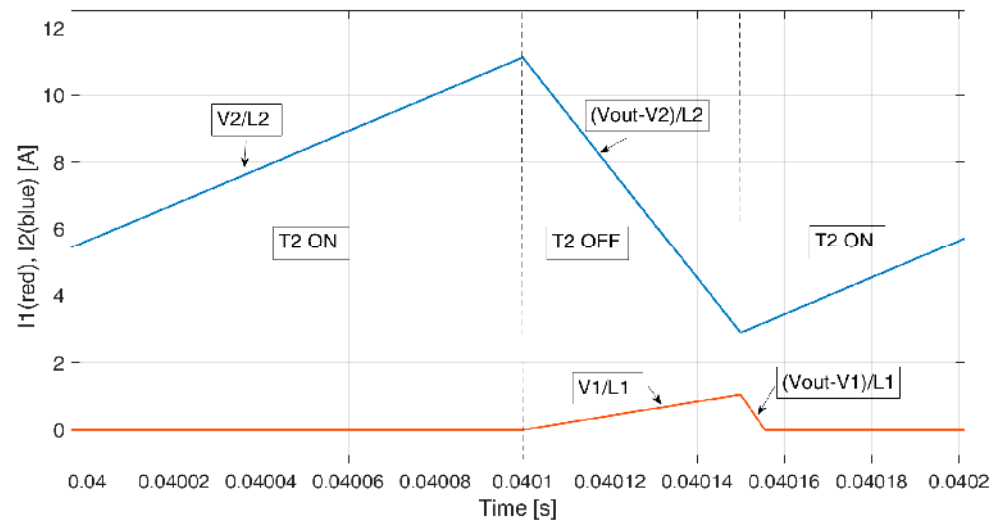


Figure A4. The i_1 (red) and i_2 (blue) waveforms when $I_1 < 0$ and $I_2 < 0$ (condition 4).

Appendix A.1.3. NOV, $I_1 < \hat{I}_1$ Equation for m_1

The current waveforms for the case where $I_1 < \hat{I}_1$ and there is NOV are shown in Figure A5. When $I_1 < \hat{I}_1$, I_1 is controlled by m_3 and t_1 is defined in (A17), the i_1 negative peak $I_{1peakNeg}$ is defined in (A18), t_2 and t_3 are defined in (A19) and (A20), the i_1 positive peak value $I_{1peakPos}$ is defined in (A21), and t_4 is defined in (A22). The average current I_1 is defined in (A23).

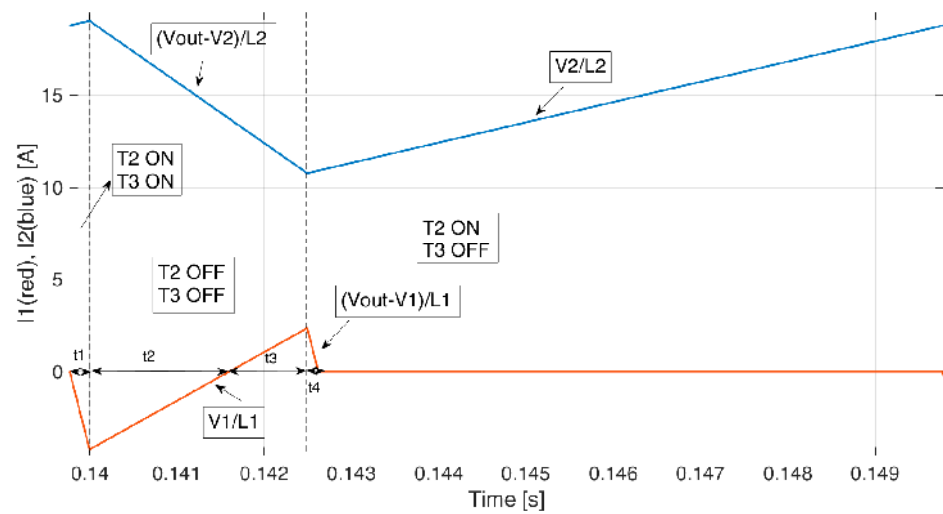


Figure A5. The i_1 (red) and i_2 (blue) waveforms when $I_1 < 0$ and $I_2 < 0$ (condition 4).

$$t_1 = m_3 T_s \quad (A17)$$

$$I_{1peakNeg} = t_1 \frac{V_{out} - V_1}{L_1} \quad (A18)$$

$$t_2 = \frac{I_{1peakNeg}}{\frac{V_1}{L_1}} \quad (A19)$$

$$t_3 = 1 - m_2 - t_2 \quad (A20)$$

$$I_{1peakPos} = \frac{V_1}{L_1} t_3 \quad (A21)$$

$$t_4 = \frac{I_{1peakPos}}{\frac{V_{out}-V_1}{L_1}} \quad (A22)$$

$$I_1 = \frac{I_{1peakPos}(t_3 + t_4) - I_{1peakNeg}(t_1 + t_2)}{2T_s} \quad (A23)$$

Substituting (A17)–(A22) in (A23), one obtains (A24).

$$m_3 = \frac{V_1(1 - m_2)}{2(V_{out} - V_1)} - \frac{L_1}{T_s V_{out}(1 - m_2)} I_1 \quad (A24)$$

Since Equation (25) is valid only when there is no superimposition (i.e., i_2 is in the CCM), $m_2 = \frac{V_{out}-V_2}{V_{out}}$, if one substitutes this value in (A24), one obtains (A25).

$$m_3 = \frac{V_1 V_2}{2V_{out}(V_{out} - V_1)} - \frac{L_1}{T_s V_2} I_1 \quad (A25)$$

Appendix A.1.4. TOV, Equation for m_1 and m_2

The other limit condition is when $i_1 = i_2$. Both current waveforms are reported in Figure A6. Neglecting the time in which one current is increasing and the other one is decreasing, one can derive (A26)–(A29), where the rising time t_{2up} , the current peak value I_{2peak} , the falling time t_{2down} , and the average current I_2 are defined, respectively.

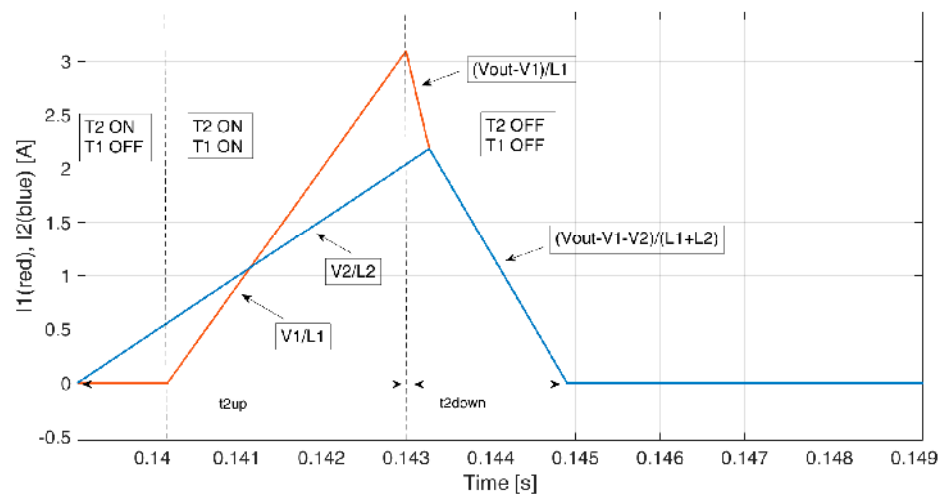


Figure A6. The i_1 (red) and i_2 (blue) waveforms when $I_1 < 0$ and $I_2 < 0$ (condition 4).

$$t_{2up} = m_2 T_s \quad (A26)$$

$$I_{2peak} = \frac{V_2}{L_2} t_{2up} \quad (A27)$$

$$t_{2down} = \frac{I_{2peak}}{\frac{V_{out}-V_1-V_2}{L_1+L_2}} \quad (A28)$$

$$I_2 = \frac{I_{2peak}(t_{2up} + t_{2down})}{2T_s} \quad (A29)$$

Combining (A26)–(A29), one obtains (A30).

$$m_2 = \sqrt{\frac{2L_2^2(V_{out} - V_1 - V_2)}{V_2 T_s (L_1 V_2 - L_2 V_1 + L_1 V_{out})}} \sqrt{I_2} \quad (A30)$$

Equation (A30) is valid until i_2 is in the DCM. Once i_2 is in the CCM, the relationship in (A31) should be verified. Substituting (A26)–(A28) in (A31), one obtains (A32), which leads to the relationship in (A33). Therefore, m_2 is given by (A30), with the limitation that $m_2 \in \left[0; \frac{L_2(V_{out} - V_1 - V_2)}{(L_1 V_2 - L_2 V_1 + L_1 V_{out})}\right]$.

$$t_{2up} = t_{2down} \quad (A31)$$

$$\frac{V_2 m_2}{L_2} = \frac{V_{out} - V_1 - V_2}{L_1 + L_2} (1 - m_2) \quad (A32)$$

$$m_2 = \frac{L_2(V_{out} - V_1 - V_2)}{L_1 V_2 - L_2 V_1 + L_1 V_{out}} \quad (A33)$$

Moreover, t_{1up} , I_{1peak} , t_{1down} , and I_1 are defined in (A34)–(A37). Substituting (A34)–(A36) in (A37), one obtains (A38).

$$t_{1up} = m_1 T_s \quad (A34)$$

$$I_{1peak} = \frac{V_1}{L_1} t_{1up} \quad (A35)$$

$$t_{1down} = \frac{I_{1peak}}{\frac{V_{out} - V_1 - V_2}{L_1 + L_2}} \quad (A36)$$

$$I_1 = \frac{I_{1peak}(t_{1up} + t_{1down})}{2T_s} \quad (A37)$$

$$m_1 = \sqrt{\frac{2L_1^2}{T_s V_1} \left(\frac{V_{out} - V_1 - V_2}{L_2 V_1 - L_1 V_2 + L_1 V_{out}} \right)} \sqrt{I_1} \quad (A38)$$

Since $i_1 = i_2$, the duty cycle should have the limitation $m_1 \in [0; m_2]$. Please note that this limit is valid if $\frac{V_2}{L_2} < \frac{V_1}{L_1}$. If $\frac{V_2}{L_2} > \frac{V_1}{L_1}$, then $m_1 \in \left[0; \frac{L_1(V_{out} - V_1 - V_2)}{(L_2 V_1 - L_1 V_2 + L_1 V_{out})}\right]$ and $m_2 \in [0; m_1]$.

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